

# Abstracts

## A 900 MHz image-reject transceiver Si bipolar IC

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*P. Katzin, A.P. Brokaw, G. Dawe, B. Gilbert, L. Lynn and J.M. Maurant. "A 900 MHz image-reject transceiver Si bipolar IC." 1998 Radio Frequency Integrated Circuits (RFIC) Symposium 98. (1998 [RFIC]): 97-100.*

An ISM-band transceiver chip includes an LNA, image-reject up- and down-converters, a VCO, a /64/65 dual-modulus prescaler, an IF Limiting amplifier/RSSI strip, and a voltage regulator. The IC operates from a single-supply down to 3.0 V and is optimized for a 10.7 MHz IF frequency to allow use of very low-cost ceramic filters. The 6.8 mm<sup>2</sup> chip is fabricated using a 24 GHz f<sub>sub T</sub>/ Si bipolar process and mounted in a 28 pin SSOP package.

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